

REMARKS

Claims 1-20 are pending in this application. Claims 1-4, 7, 8, and 20 are amended. No claims are cancelled. Favorable reconsideration of this application, in view of the present amendment and in light of the following discussion, is respectfully requested.

Applicants note with appreciation the Examiner's acknowledgement of the references cited in the Information Disclosure Statement filed with the application on September 23, 2003.

Applicants also note with appreciation the Examiner's indication that the drawings and foreign priority documents filed on September 23, 2003 have been received and accepted by the U.S. Patent and Trademark Office.

ALLOWABLE SUBJECT MATTER

Applicants note with appreciation the Examiner's indication in paragraph 32 of the April 19, 2005 Office Action that claims 9-19 are allowed.

CLAIM REJECTIONS UNDER 35 U.S.C. § 102 AND 35 U.S.C. § 103

In the April 19, 2005 Office Action, claims 1-4, 6, 7, and 20 stand rejected under 35 U.S.C. § 102(e) as anticipated by Jeung et al. (U.S. Patent No. 6,750,547, herein Jueng '547 patent); claims 1-4, 7, 8, and 20 stand rejected under 35 U.S.C. § 102(e) as anticipated by Jeung et al. (U.S. Patent No. 6,747,348, herein Jueng '348 patent); claims 1-7 stand rejected under 35 U.S.C. § 103(a) as unpatentable over Uchida (U.S. Patent No. 6,437,446) in view of the Jueng '547 patent; claim 8 stands rejected under 35 U.S.C. § 103(a) as unpatentable over Uchida in view of the Jueng '547 patent, further in view of the Jueng '348 patent; and claim 8 stands rejected under 35 U.S.C. § 103(a) as unpatentable over the Jueng '547 patent in view

of the Jueng '348 patent. The Applicants respectfully traverse the rejections for the reasons detailed below.

As noted above, paragraph 32 of the April 19, 2005 Office Action indicates claims 9-19 are allowed. In light of the Examiner's indication that claims 9-19 are allowed, independent claims 1 and 20 are amended to include features similar to independent claim 9.

More specifically, the semiconductor package of amended claim 1 recites, *inter alia*, "a second peripheral sealing portion formed along the first peripheral sealing portion covering the at least one first chip pad and a portion of the at least one pattern lead without covering at least one second chip pad of the plurality of chip pads." Similarly, the method for manufacturing a semiconductor package of amended claim 20 recites, *inter alia*, "forming a second peripheral sealing portion along the first peripheral sealing portion covering the at least one first chip pad and a portion of the at least one pattern lead without covering at least one second chip pad of the plurality of chip pads." Applicants respectfully submit that the cited references of Jueng '547 patent, Jueng '348 patent, and Uchida, either alone or in any proper combination, do not disclose, teach, or suggest at least the above-identified features of amended independent claims 1 and 20.

Evidence of the above assertion is provided by the Examiner's indication that independent claim 9 reciting "a second peripheral sealing portion formed along the first peripheral sealing portion and the first pattern leads; and a plurality of second pattern leads providing electrical connections between a second group of chip pads and a second group of substrate pads, the second pattern leads extending along an inclined surface of the second peripheral sealing portion" is allowable. Further, the Applicants can find no disclosure, teaching, or suggestion in any of the cited references regarding a second peripheral sealing portion as recited in amended claims 1 and 20.

Therefore, Applicants respectfully request that the rejections of claims 1-8 and 20 under 35 U.S.C. § 102(e) and 35 U.S.C. § 103(a) be withdrawn.

CONCLUSION

Accordingly, in view of the above amendments and remarks, reconsideration of the rejections of claims 1-8 and 20 in connection with the present application is earnestly solicited.

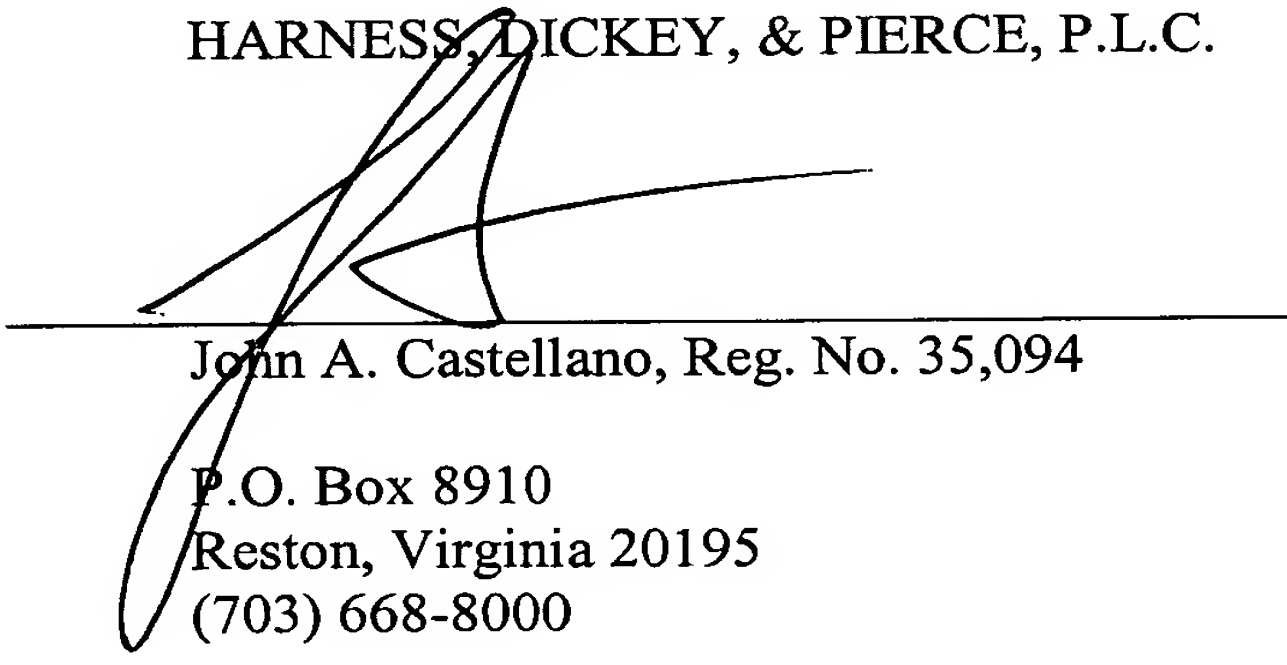
Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact John A. Castellano at the telephone number of the undersigned below.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNES, DICKEY, & PIERCE, P.L.C.

By


John A. Castellano, Reg. No. 35,094

P.O. Box 8910
Reston, Virginia 20195
(703) 668-8000

JAC/SAE/pjd